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paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment.Pages Number: 271 Publisher: Tsinghua University. Pub. Date :2011-09-01 version 2. Renxiang Min. Wang Dynasty. Zong-kui edited the Practical Guide to Computer Interface Technology (2nd Edition) a systematic introduction and application of micro-computer interface technology . The book content is divided into 10 chapters. the main contents include micro-computer system. input and output interface. timer / counting technique. interruption technology. DMA technology and I / O interface technology. has introduced the current widespread use of PC's USB and PCI bus interface technology the latest knowledge. Practical Guide to Computer Interface Technology (2nd Edition) to highlight the application. focus on skills as the goal. concise. system-oriented. application-oriented. example-rich. with each chapter are questions and unit test and standardization reference to the answer. easy to unit test and review readers. Practical Guide to Computer Interface Technology (2nd Edition) suitable for all kinds of colleges and universities teaching computer science and engineering undergraduate teaching reference books. but also the training of adult education as institutions of higher learning or self-reference. Contents: Chapter 1 Microcomputer System 1.1 micro micro computer system hardware architecture 1.1.1 1.1.2 basic components of computer systems micro-computer system structure 1.1.3 performance micro-computer system microprocessor 1.2.18086/8088 1.2 microprocessor microprocessor 1.2.380486 to 1.2.280386 Pentium 4 microprocessor unit tests in Chapter 2 Micro-computer interface technology base 2.1 Interface Technical Overview 2.1.1 Interface Definition 2.1.2 Features 2.1.3 interface Classification Interface 2.2 interface to transfer information in the data and its components 2.2.1 2.2.3 2.2.2 status information interface circuit control information 2.2.4 The general structure 2.3 interface port addressing and decoding 2.3.1 Classification 2.3.2I / O ports The addressing method 2.3.3I / O port address decoding method 2.3.4CPU the I / O instructions 2.4CPU...

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